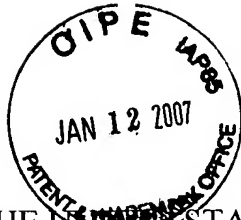


0321.67590



PATENT APPLICATION

*15W*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chung-Kuan Cheng

Serial No.: 10/526,523

Conf. No.: 5310

Filed: March 1, 2005

For: INTERCONNECTION ARCHITECTURE  
AND METHOD OF ASSESSING  
INTERCONNECTION  
ARCHITECTURE

Art Unit: 2818

Examiner: Nguyen, Thinh T.

*I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.*

Jan. 9, 2007  
Date

*Arik B. Ranson*  
Registration No. 43,874  
Attorney for Applicant

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL

Dear Sir:

Transmitted herewith is a communication regarding the above-identified application.

(X) Response to Election/Restriction Requirement.

(X) Petition for Extension of Time (in duplicate) with a check in the amount of \$120.00

(X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.

(X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

January 9, 2007  
300 South Wacker Drive – Suite 2500  
Chicago, Illinois 60606  
Tel.: (312) 360-0080  
Fax: (312) 360-9315  
Customer No.: 24978

GREER, BURNS & CRAIN, LTD.

By: *Arik B. Ranson*  
Arik B. Ranson, Reg. No. 43,874